



G30_0_h/l signals are constants, therefore circuitry is not needed. The G30_0_h/l signals are generated at G30_h_S and G30_l_S

INPUTS		OUTPUTS	
VDD	○VDD	GND	○GND
CLK	○CLK	SUM_0B_HO	○SUM_0B_H_MEASURE
CLK_INV	○CLK_INV	SUM_0B_LO	○SUM_0B_L_MEASURE
Cin_1	○Cin_1	SUM_1B_HO	○SUM_1B_H_MEASURE
Cin_1	○Cin_1	SUM_1B_LO	○SUM_1B_L_MEASURE
G-1_0_h_IN_SIG	○G-1_0_h_IN_SIG	SUM_2B_HO	○SUM_2B_H_MEASURE
G-1_0_l_IN_SIG	○G-1_0_l_IN_SIG	SUM_2B_LO	○SUM_2B_L_MEASURE
A1_1_TS	○A1_1	SUM_3B_HO	○SUM_3B_H_MEASURE
A1_1_TS	○A1_1	SUM_3B_LO	○SUM_3B_L_MEASURE
A2_1_TS	○A2_1	SUM_4B_HO	○SUM_4B_H_MEASURE
A2_1_TS	○A2_1	SUM_4B_LO	○SUM_4B_L_MEASURE
A3_1_TS	○A3_1	SUM_5B_HO	○SUM_5B_H_MEASURE
A3_1_TS	○A3_1	SUM_5B_LO	○SUM_5B_L_MEASURE
A4_1_TS	○A4_1	SUM_6B_HO	○SUM_6B_H_MEASURE
A4_1_TS	○A4_1	SUM_6B_LO	○SUM_6B_L_MEASURE
A5_1_TS	○A5_1	SUM_7B_HO	○SUM_7B_H_MEASURE
A5_1_TS	○A5_1	SUM_7B_LO	○SUM_7B_L_MEASURE
A6_1_TS	○A6_1		
A6_1_TS	○A6_1		
A7_1_TS	○A7_1		
A7_1_TS	○A7_1		
A8_1_TS	○A8_1		
A8_1_TS	○A8_1		
A9_1_TS	○A9_1		
A9_1_TS	○A9_1		